

What is claimed is:

- 1 1. A state-displaying device for displaying state data generated by a data-processing
- 2 device, said state-displaying device comprises:
 - 3 a universal asynchronous receiver/transmitter interface for receiving state data
 - 4 for outputting said state data in a serial mode; and
 - 5 a displaying device including:
 - 6 a microprocessor coupled to said universal asynchronous
 - 7 receiver/transmitter interface for outputting a displaying signal in corresponding to
 - 8 said state data output by said universal asynchronous receiver/transmitter interface;
 - 9 and
 - 10 a multi-segment display module coupled to said microprocessor for
 - 11 displaying a symbol in corresponding to said displaying signal.
- 1 2. The state-displaying device as in claim 1, wherein said multi-segment display
- 2 module at least is composed of a seven- segment display.
- 1 3. The state-displaying device as in claim 1, wherein said data-processing device is
- 2 selected between a server and a personal computer.
- 1 4. The state-displaying device as in claim 1, wherein said symbol is selected among a
- 2 numeral, an English letter and a specific character.
- 1 5. The state-displaying device as in claim 1, wherein said universal asynchronous
- 2 receiver/transmitter interface includes a data transmitting line Tx, a data receiving
- 3 line Rx, a power line and a grounding line (Gnd).
- 1 6. The state-displaying device as in claim 1, wherein said state data output by said
- 2 universal asynchronous receiver/ transmitter interface is of a specification of
- 3 RS-232.
- 1 7. The state-displaying device as in claim 1, wherein said state-displaying device is

- 2 connected externally to a serial port.
- 1 8. The state-displaying device as in claim 1, wherein said state data includes an on/off
2 bit, at least a command mode bit and a plurality of displaying bits, said command
3 mode bit is used to define a mode of displaying of said displaying bits, said
4 microprocessor decides a mode of displaying of said multi- segment display module
5 according to said mode of displaying of said displaying bits.
- 1 9. The state-displaying device as in claim 8, wherein said command mode bit is used
2 to decide between a searching mode and a following-the-sequence mode.
- 1 10. The state-displaying device as in claim 9, wherein when said microprocessor
2 decides that a mode of displaying of said multi-segment display module is said
3 searching mode, it makes said multi-segment display module to display said symbol
4 according to said displaying signal which is generated by searching in a table
5 according to values of said displaying bits.
- 1 11. The state-displaying device as in claim 10, wherein said symbol is selected among
2 a numeral, an English letter and a specific character.
- 1 12. The state-displaying device as in claim 9, wherein when said microprocessor
2 decides that a mode of displaying is said following-the-sequence mode, a selecting
3 bit of said displaying bits is used to designate a seven-segment display to be enabled,
4 and said enabled seven-segment display is rendered to display said symbol according
5 to the state of a plurality of segment- selecting bits of said displaying bits.
- 1 13. The state-displaying device as in claim 12, wherein said symbol is selected among
2 a numeral, an English letter and a specific character.
- 1 14. The state-displaying device as in claim 1, wherein said state data is generated by a
2 BIOS program of said data-processing device.
- 1 15. The state-displaying device as in claim 1, wherein said state data is generated by a

- 2 detecting application program of said data-processing device.
- 1 16. The state-displaying device as in claim 15, wherein said detecting application
- 2 program is executed in an operating system of said data-processing device.